Sigasi ®
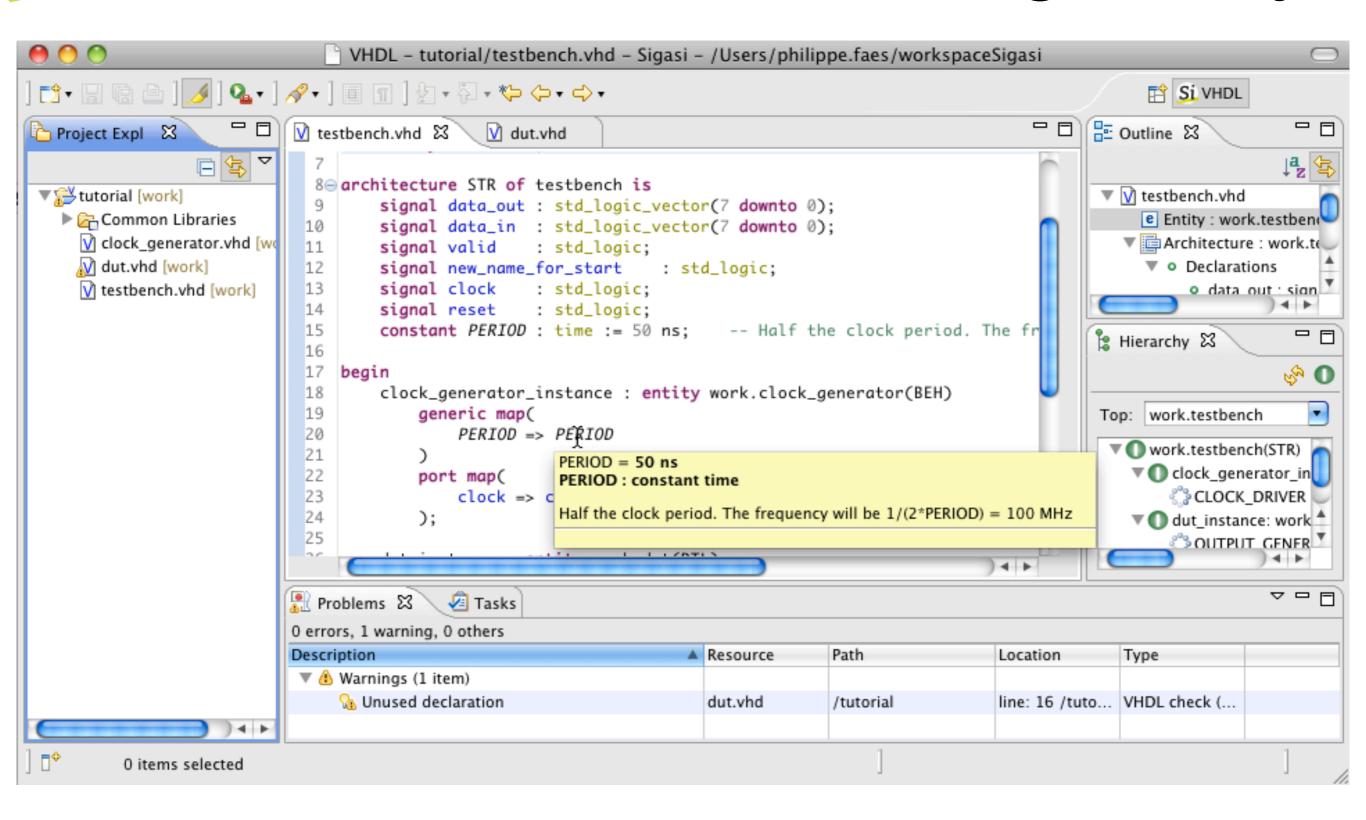
Modern VHDL design

Hendrik Eeckhaut

Sigasi

EclipseCon

Talk: "Bringing the power of Eclipse to VHDL designers": Thursday, 14:15

- Hardware designers use simple editors

- Digital design is hard and error prone

- Software guys have much better tools

VHDL design entry

Secret sauce

- **Type time compiler**

- Direct feedback

- Read output of other tools (ModelSim, ...)

- IDE *thinks* along

Key Features

- Intelligent navigation

- Instant error reporting (faster feedback)

- Intelligent code completion

- Linting + Quick Fix

- Automated compile step

- All features work, even if code is incomplete (important for legacy projects)

Selected customers

Customers worldwide, in healthcare, telecom, consumer electronics, industrial automation, automotive, aerospace and defense.

Product Offering

- Sigasi 2.x Starter Edition

  - free

- Sigasi 2.x Pro

  - € 600 / license / year

  - Floating or node locked

- Training & Support

  - custom

- Available as plugin and stand-alone

Custom add-ons:

- Integration with tool flow

- Code review tool

- Style guide checker

- Project templates / code templates

- Issue tracker, Version control system

- . . .

Sigasi in PolarSys?

- VHDL

- Build on Eclipse technology (Xtext, EMF, ...)

- Commercial, closed source